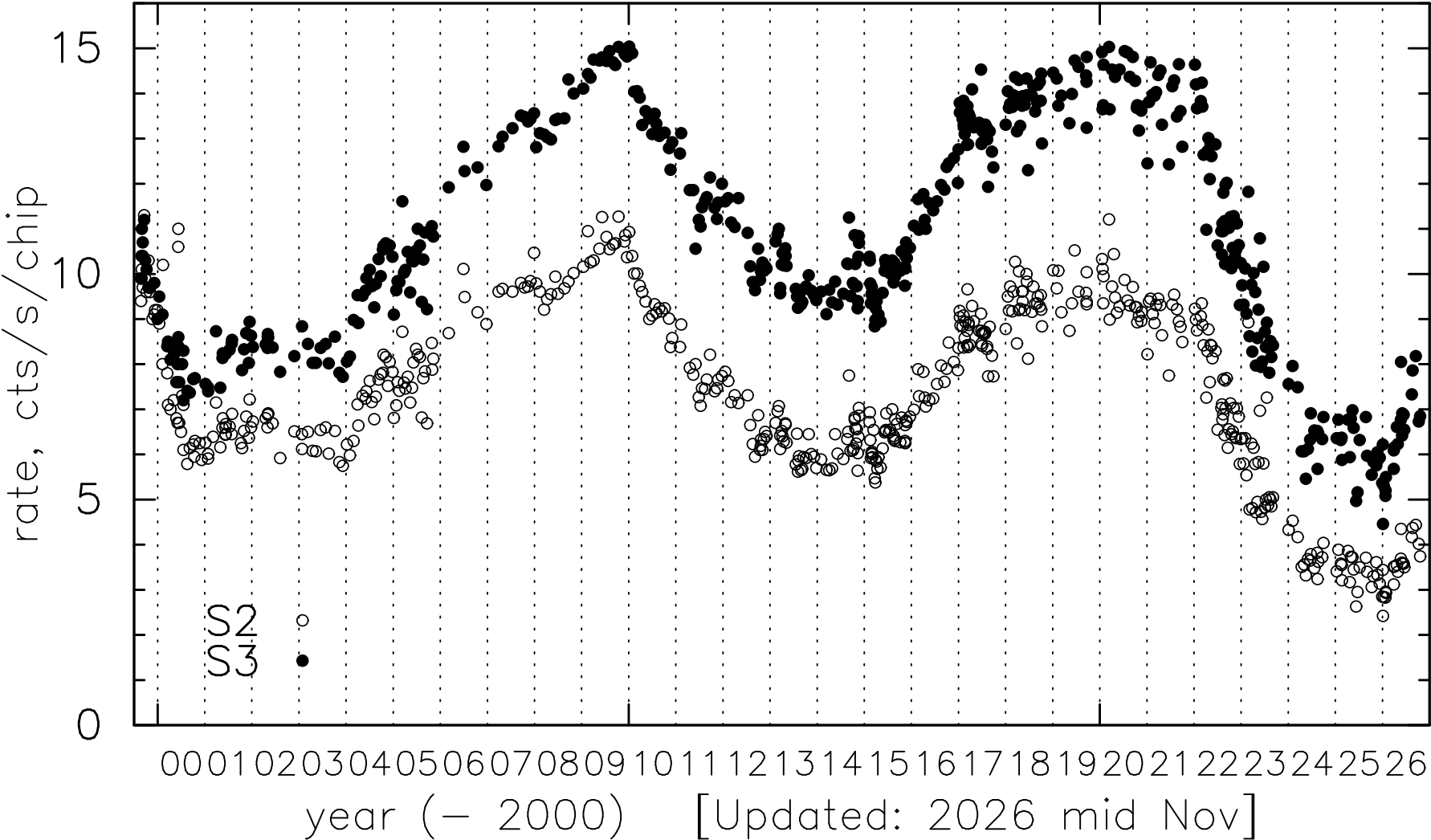
15
10
5
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rate, cts/s/chip
S2
S3
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year (− 2000) [Updated: 2026 mid Nov]